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Paper Title: An Optimized Switching Patterns for Reducing a Switching Loss of a Matrix Converter under Any Power Factor Conditions

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Junnosuke Haruna received the B.E., M.E., and Ph.D. degrees in electrical engineering from Nagaoka University of Technology, Japan, in 2006, 2008, and 2011, respectively. He joined the Department of Electrical Engineering, Faculty of Science and Technology, Tokyo University of Science, Noda, Japan, as an Assistant Professor in 2011. In 2014, he joined the Department of Electrical Engineering, Utsunomiya University, Japan. His research interests include power electronics, especially AC/AC power conversion. He is a member of IEEE.