

Paper ID: 110-FPGA-based FDNE Models for the Accurate Real-time Simulation of Power Systems in Aircrafts



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Fahimeh Hajizadeh received her B.Sc. in computer engineering from Shahid Beheshti University, Tehran, Iran, in 2015, and her M.Sc. in computer architecture engineering from Kharazmi University, Tehran, Iran, in 2019. During her studies, she contributed to published papers and undertook the role of a reviewer for the Journal of Supercomputing. She is currently pursuing a Ph.D. at Polytechnique Montreal, focusing on FPGA-based FDNE models for real-time simulation of aircraft power systems. Her internship at OPAL-RT enriches her research endeavors, aligning seamlessly with her passion for the field of real-time simulation.